

# PATENT ABSTRACTS OF JAPAN

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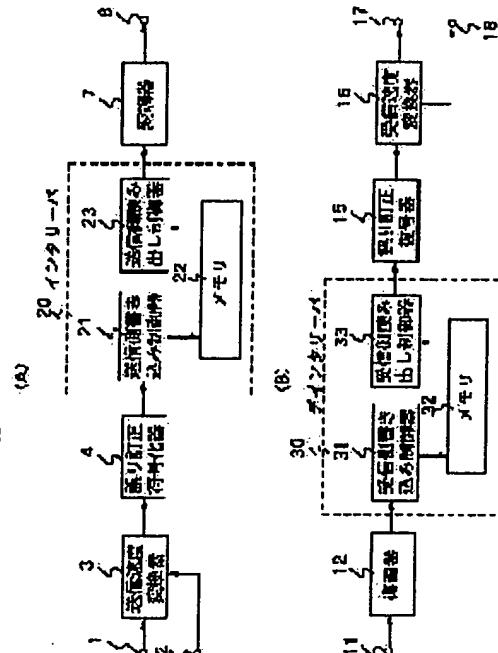
(72)Inventor : MIZOGUCHI SHOICHI

## (54) BIT INTERLEAVE TRANSMISSION SYSTEM

### (57)Abstract:

PURPOSE: To provide a bit interleave transmission system which is applied to the digital radio communications and can transmit even the auxiliary signal data that are fixed at the same value without causing any abnormal spectrum to a modulated wave.

CONSTITUTION: A write controller 21 shifts an input data string to the right by one bit for each frame against a memory 22 by means of a write start address. Then the controller 21 writes a data train on the auxiliary signal of each frame with the bit data positions shifted in sequence. A read controller 23 reads the stored data string out of the memory 22 in sequence and in the depth direction and takes out an interleaved data string. A write controller 31 and a read controller 33 of the receiver side reverse the operations of both controllers 21 and 23 to take the deinterleaved data out of a memory 32.



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